ELECTRIC DOUUBLE LAYER CAPACITORS

PRODUCT SPECIFICATION

規格書

CUSTOMER: DATE:

(客戶) (日期): 2019-7-23

CATEGORY (品名) : ELECTRIC DOUBLE LAYER CAPACITORS

DESCRIPTION (型号) : DRL 2.7V15F (φ12.5x30)

VERSION (版本) : 01

Customer P/N : /

SUPPLIER : /

SUPPLIER					
PREPARED (拟定)	CHECKED (审核)				
杜焕	刘渭清				

CUSTOMER			
APPROVAL	SIGNATURE		
(批准)	(签名)		

	SPECIFICATION DBL SERIES				ALTERN	ATION HIS	TORY
DRL SERIES				RECORDS	Ι.		
Rev.	Date	Mark	Page	Contents	Purpose	Drafter	Approver

Issued-date: 2017-07-28	Name	Specification Sheet – DRL				
Version	01		Page	1		
STANDARD MANUAL						

CONTENTS

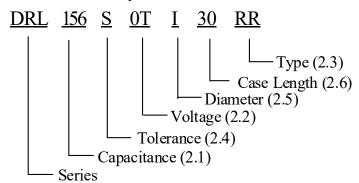
	Sheet
1. Application	3
2. Part Number System	3
3. Characteristics	4~10
3.1 Rated voltage & Surge voltage	
3.2 Capacitance (Tolerance)	
3.3 ESR	
3.4 Leakage current	
3.5 Temperature characteristic	
3.6 Load life test	
3.7 Damp heat test	
3.8 Lead strength	
3.9 Resistance to vibration	
3.10 Solderability	
3.11 Resistance to soldering heat	
4. Product Dimensions	11
5. Notice item	12

Issued-date: 2017-07-28	Name	Specification Sheet – DRL				
Version	01		Page	2		
STANDARD MANUAL						

1. Application

The specification applies to electric double layer capacitors used in electronic equipment.

2. Part Number System



2.1 <u>Capacitance code</u>

Code	156
Capacitance (F)	15

2.2 Rated voltage code

Code	0T
Voltage (W.V.)	2.7

2.3 <u>Type</u>

Code	RR
Type	Bulk

2.4 <u>Capacitance tolerance</u>

"S" stands for -20% ~ 50%

2.5 <u>Diameter</u>

Code	I
Diameter	12.5

2.6 <u>Case length</u>

30=30mm

Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	3	
STANDARD MANUAL					

3. Characteristics

Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests is as follows:

Ambient temperature: 15°C to 35°C
Relative humidity : 25% to 75%
Air Pressure : 86kPa to 106kPa

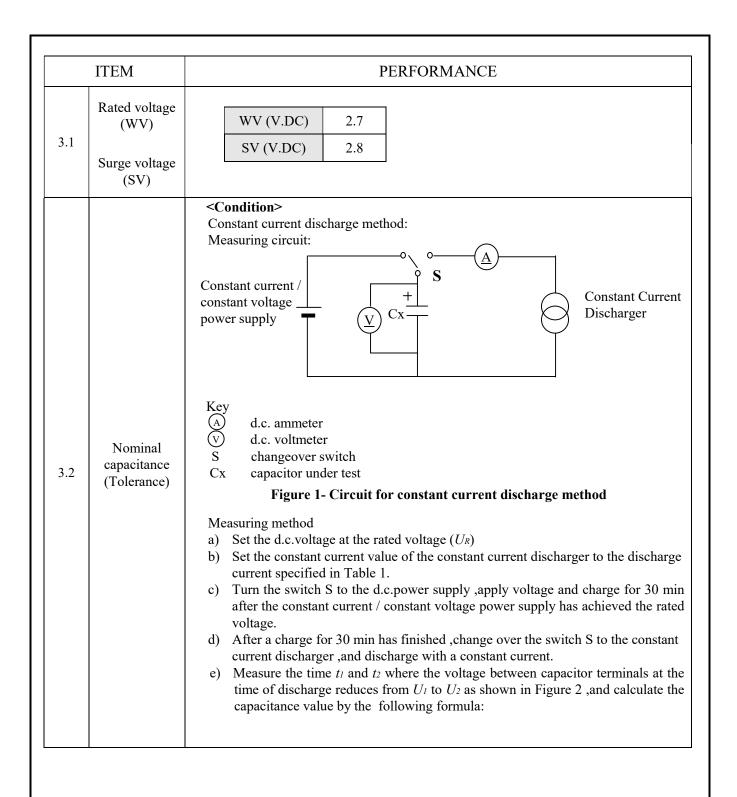
If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature: $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

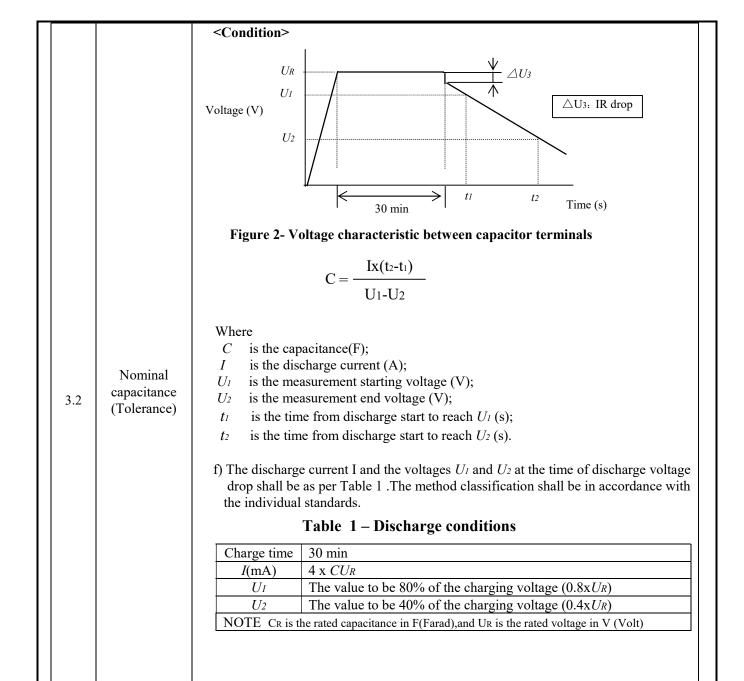
Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage is -40°C to 70°C.

Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	4	
STANDARD MANUAL					



Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	5	
STANDARD MANUAL					



Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	6	
STANDARD MANUAL					

3.3	ESR	Measur Measur <crite< th=""><th>ing frequency :1kHz ing temperature:20±2°C ring point :2mm max wire. ria> Less than the initial limit:</th><th>x from the surface o</th><th>f a sealing resin on the lead</th></crite<>	ing frequency :1kHz ing temperature:20±2°C ring point :2mm max wire. ria> Less than the initial limit:	x from the surface o	f a sealing resin on the lead							
3.4	Leakage current	2.The 6 3. Desi <criter i≤0.045<="" less="" td="" th=""><td>electrification time: $25^{\circ}\text{C} \pm 20^{\circ}\text{C}$ stance value of protective ia> an the initial limit($25^{\circ}\text{C} \pm 20^{\circ}\text{C}$</td><td>resistor less than 1Ω</td><td>! .</td></criter>	electrification time: $25^{\circ}\text{C} \pm 20^{\circ}\text{C}$ stance value of protective ia> an the initial limit($25^{\circ}\text{C} \pm 20^{\circ}\text{C}$	resistor less than 1Ω	! .							
		<condition></condition>										
		STEP 1	Temperature(°C) 20±2	Item Capacitance, ESR	Characteristics							
											Δ C/C	Within ±30% of initial capacitance
		2	-40+3	ESR	Less than or equal to 4 times of the value of item 3.3							
3.5	Temperature					3	Keep at 15 to 35°C for 15 minutes or more					
	characteristic	4	70±2	Δ C/C	Within ±30% of initial capacitance							
		4	70±2	ESR	The limit specified in 3.3							
			-40°C/ ESR 20°C: ESR ration 20°C: Capacitance chang									

Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	7	
STANDARD MANUAL					

	<pre>voltage for 1000 +48/0 ii </pre>	nours .The result should meet the following table:	
		Item	Performance
		Capacitance Change	Within ±30% of initial capacitance
3.6	Load life	ESR	Less than or equal to 4 times of the value of item 3.3
,.6	test	Appearance	No visible damage and no leakage of electrolyte
		-	exposed for 240±48 hours in an atmosphere of 90~95%RH stic change shall meet the following requirement.
		<criteria></criteria>	
		Item	Performance
	Damp	Capacitance Change	Within ±30% of initial capacitance
3.7	Damp heat test		

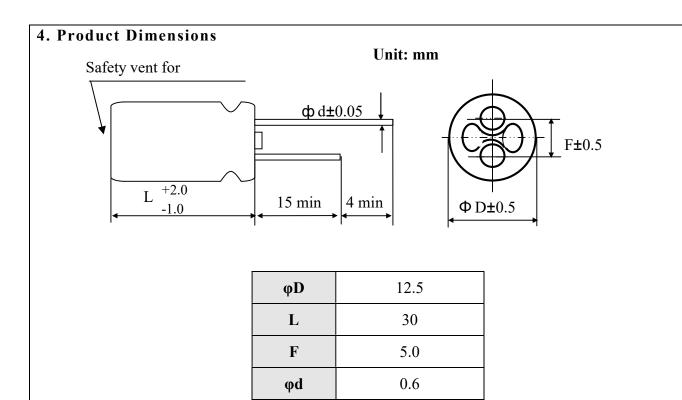
Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	8	
STANDARD MANUAL					

		a) Lead pull strength A static load force shall be applied to the terminal in the ax	ial direction and acting
		in a direction away from the body for 10 ± 1 s. Lead wire diameter (mm) Load for	roa (N)
		$0.5 < d \le 0.8$	` '
	0.3 < 4 _0.0	o	
3.8	Lead strength	b) Lead bending When the capacitor is placed in a vertical position and the table above is applied to one lead and then the capacitor is horizontal position and then returned to a vertical position for 2~3 seconds. The additional bends are made in the opposite direction	slowly rotated 90° to a
		Lead wire diameter (mm) Load forc	e (N)
		$0.5 < d \le 0.8$	
		Performance: The characteristic shall meet the following v	alue after a) or b) test.
		Item Performance	
		Capacitance Change Within ±30% of initial cap	
		Appearance No visible damage Legible leakage of electrolyte	marking and no
		Tourings of electronyte	
3.9	Resistance to vibration	Frequency: 10 to 55 Hz (1minute interval / 10 → 55 → 10Hz Amplitude: 0.75mm(Total excursion 1.5mm) Direction: X、Y、Z(3 axes) Duration: 2hours/ axial (Total 6 hours) The capacitors are supported as the following Fig2 Fig2 Performance: Capacitance value shall not show drastic change capacitance when the value is measured within 30 minutes. Priexam, Capacitance difference shall be within ±10% compared exam.	compared to the initial or to the completion of

Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	9	
STANDARD MANUAL					

3.10	Solderability	The capacitor shall be tested under the following conditions: Solder : Sn-3Ag-0.5Cu Soldering temperature: 245±3°C Immersing time : 2.0±0.5s Immersing depth : 1.5~ 2.0mm from the root. Flux : Approx .25% rosin Performance: At least 75% of the dipped portion of the terminal shall be covered with new solder.
3.11	Resistance to soldering heat	A) Solder bath method Lead terminals of a capacitor are placed on the heat isolation board with thickness of 1.6±0.5mm. It will dip into the flux of isopropylachol solution of colophony. Then it will be immersed at the surface of the solder with the following condition: Solder : Sn-3Ag-0.5Cu Soldering temperature : 260 ±5°C Immersing time : 5±0.5s Heat protector: t=1.6mm glass –epoxy board B) Soldering iron method Bit temperature : 350 ±10°C Application time : 3.5 ±0.5 s Heat protector: t=1.6mm glass –epoxy board For both methods, after the capacitor at thermal stability, the following items shall be measured: Item Performance Capacitance Change Within ±10% of initial capacitance No visible damage legible marking and no leakage of electrolyte

Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	10	
STANDARD MANUAL					



Issued-date: 2017-07-28	Name	Specification Sheet – DRL			
Version	01		Page	11	
STANDARD MANUAL					

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- (1) The capacitor has fixed polarity.
- (2) The capacitor should be used under rated voltage.
- (3) The capacitor should not be used in the charge and discharge circuit with high frequency.
- (4) The ambient temperature affects the super capacitor life.
- (5) Voltage reduction $\Delta V=IR$ will happen at the moment of discharge.
- (6) The capacitor cannot be stored on the place with humidity over 85%RH or place with toxic gas.
- (7) The capacitor should stored in the environment within -30°C~50°C temperature and less than 60% relative humidity.
- (8) If the capacitor is applied on the double-side PCB, the connection should not be around the place on which the super capacitor can contact.
- (9) Don't twist capacitor or make it slanting after installing.
- (10) Need avoid over heat on the capacitor during soldering (The temperature should be 260°C with the time less than 5s during soldering on 1.6mm printed PCB.)
- (11) There is voltage balance problem between each capacitor unit during series connection between super capacitor.

Issued-date: 2017-07-28	Name	Specification Sheet – DRL		
Version	01		Page	12
STANDARD MANUAL				